

FIG. 1
(PRIOR ART)

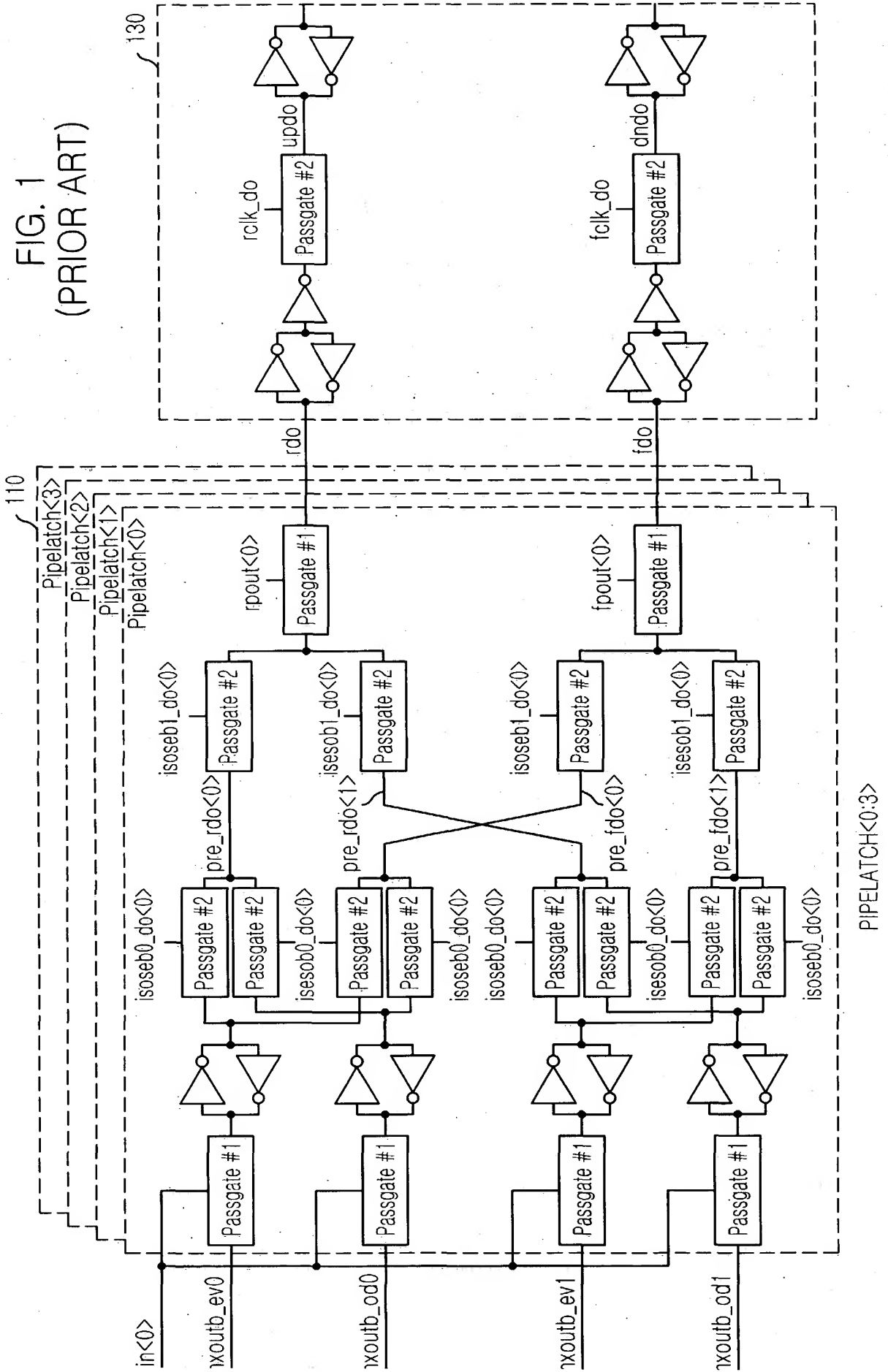


FIG. 2A
(PRIOR ART)

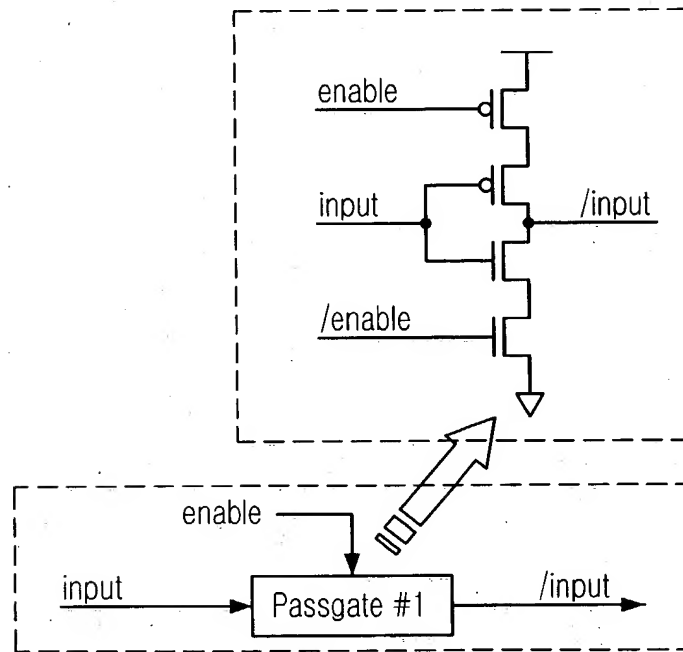


FIG. 2B
(PRIOR ART)

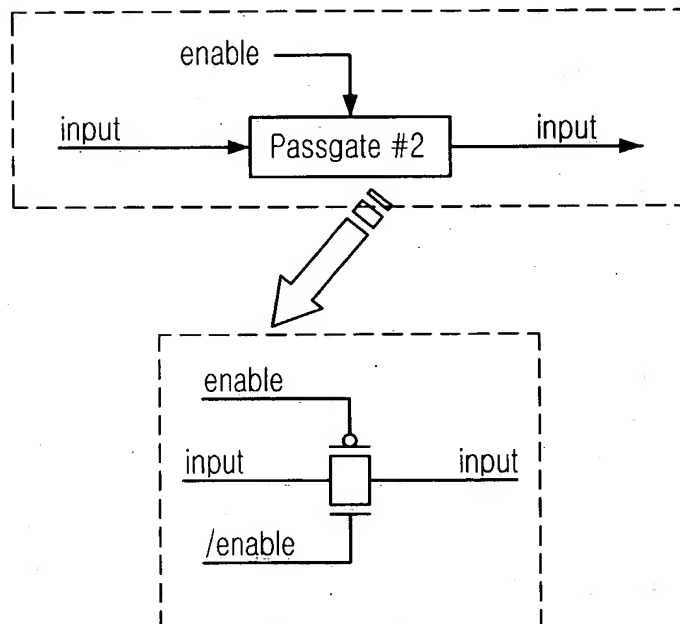


FIG. 3
(PRIOR ART)

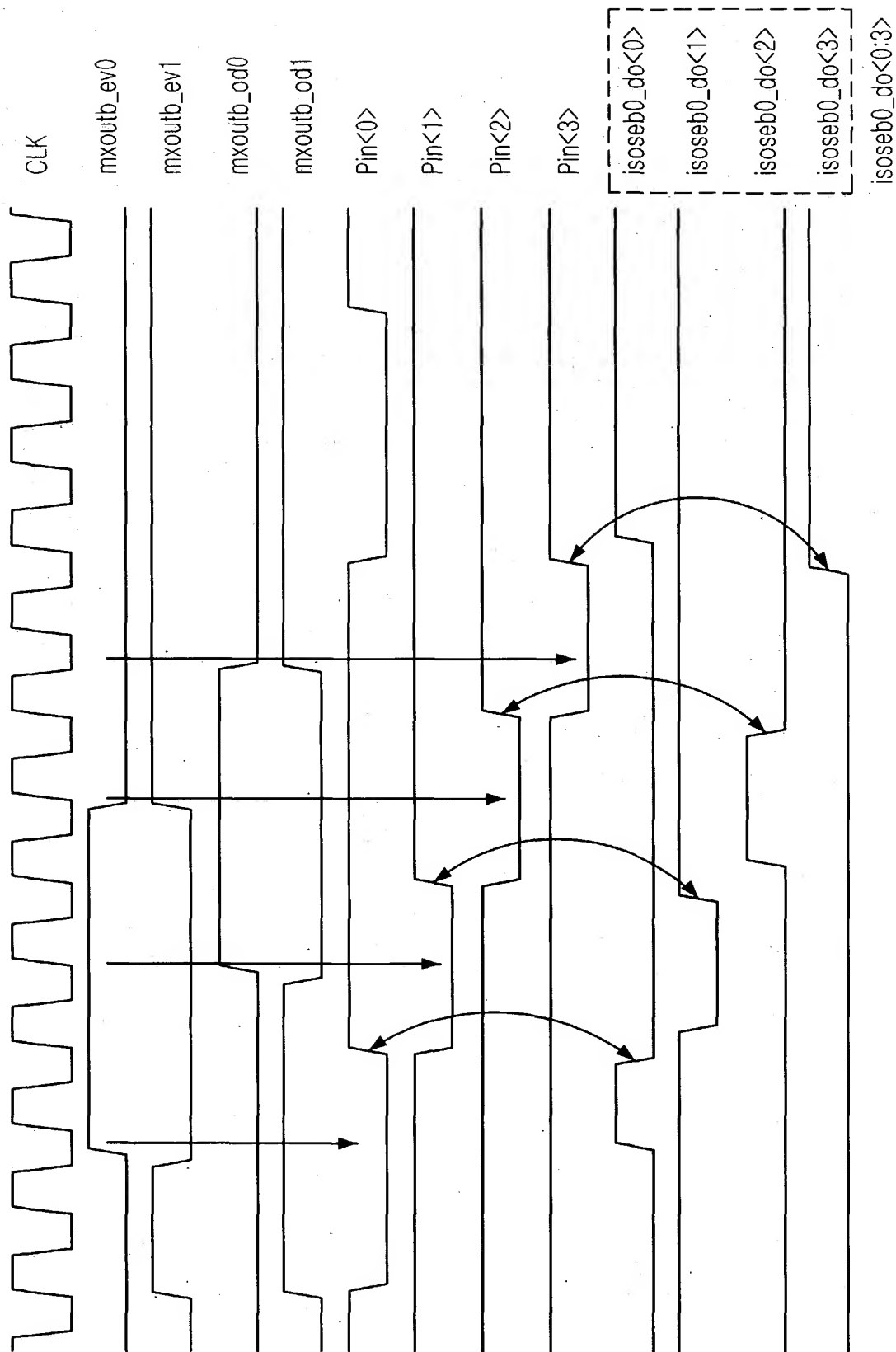


FIG. 4
(PRIOR ART)

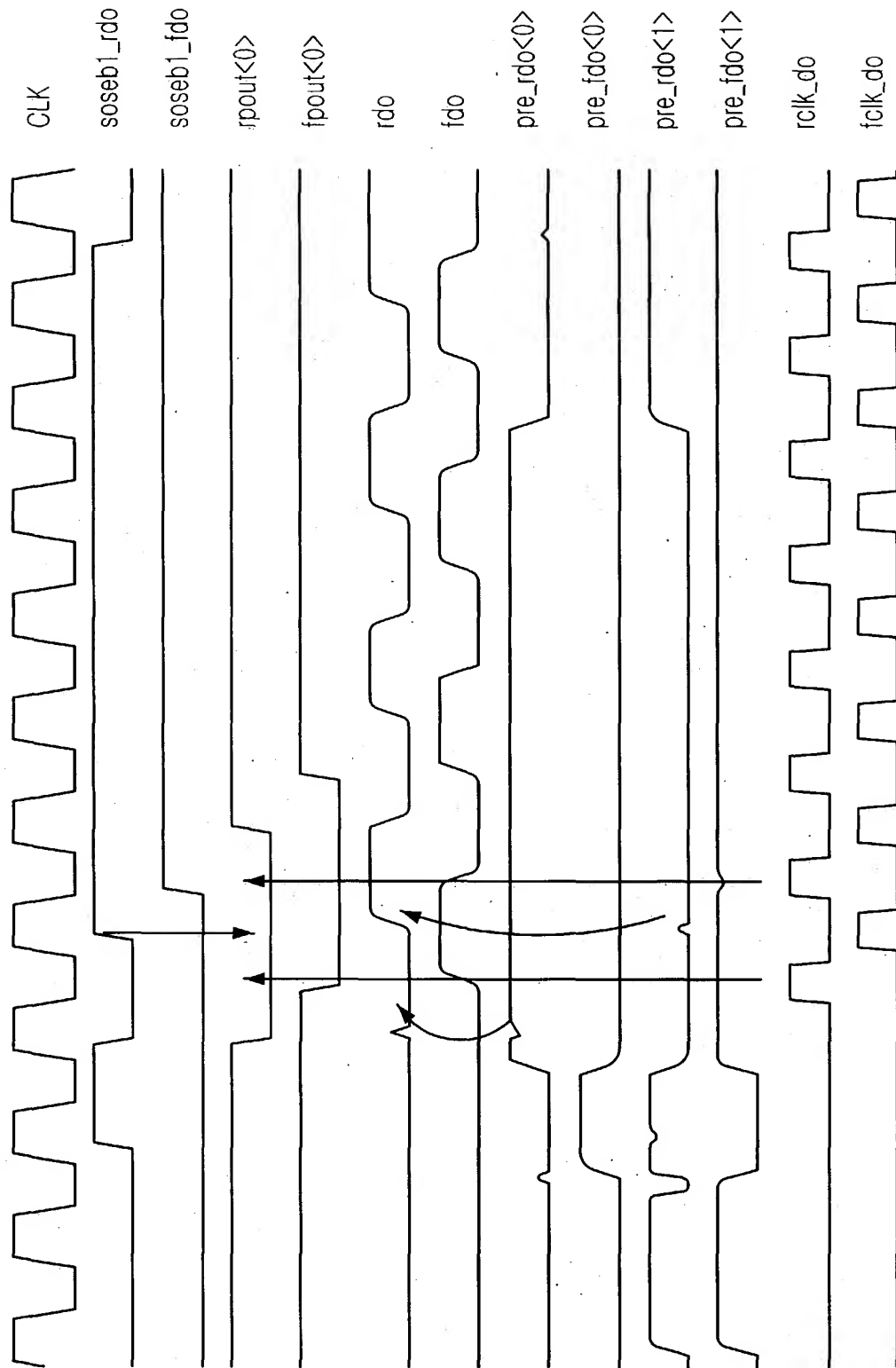
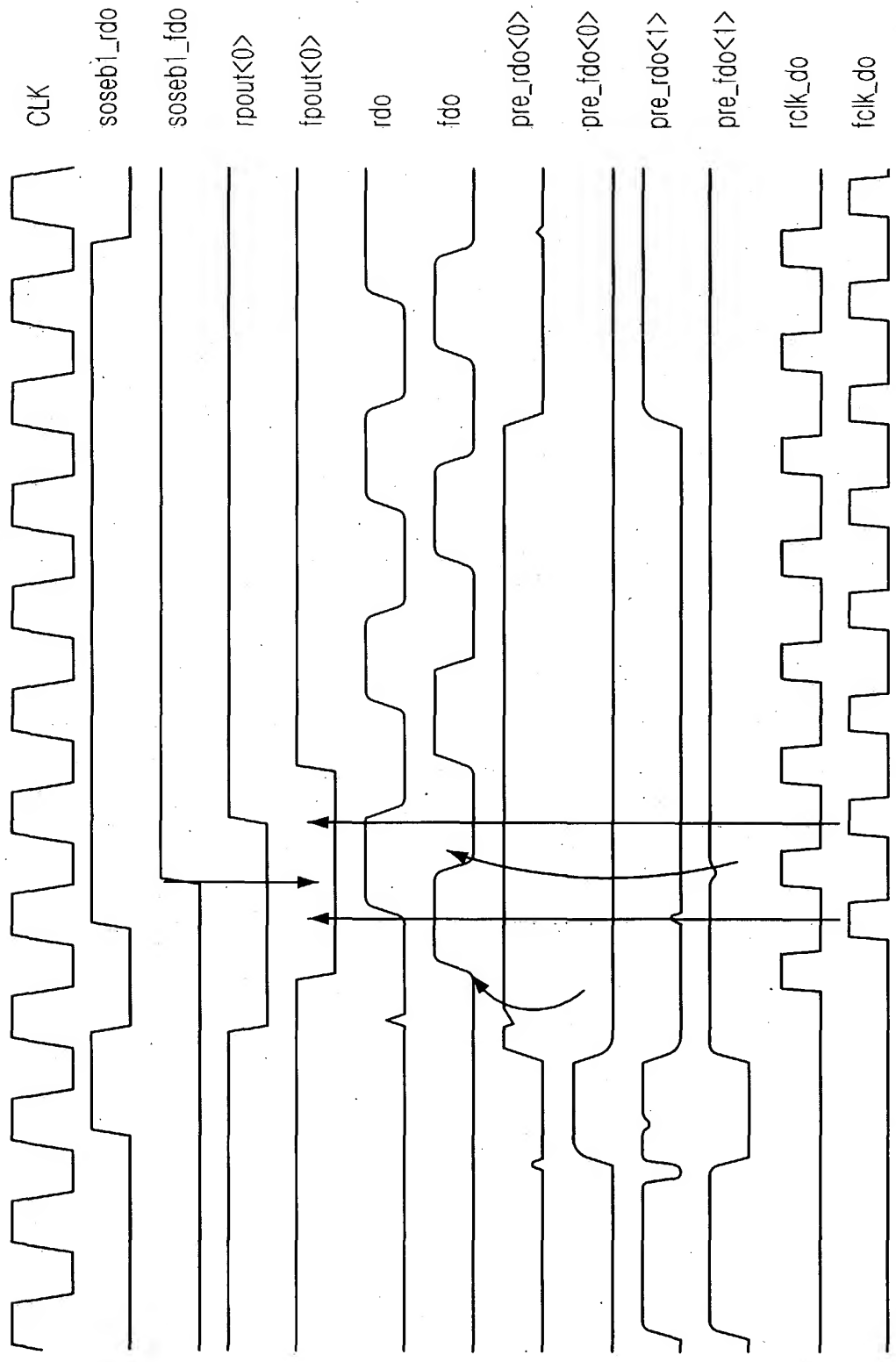


FIG. 5



start address=0

FIG. 6A

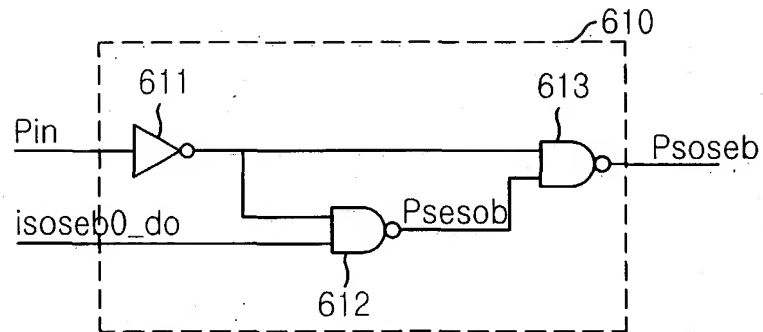


FIG. 6B

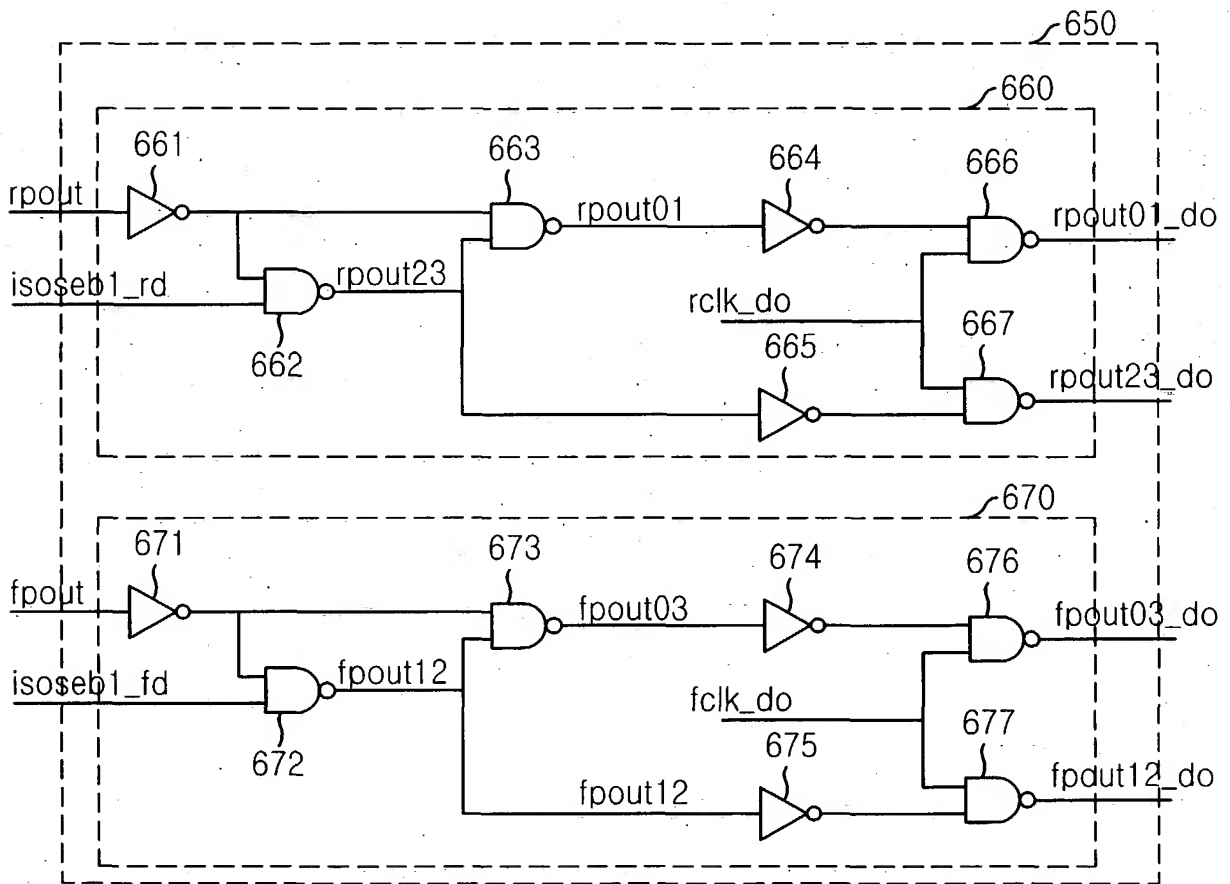


FIG. 7

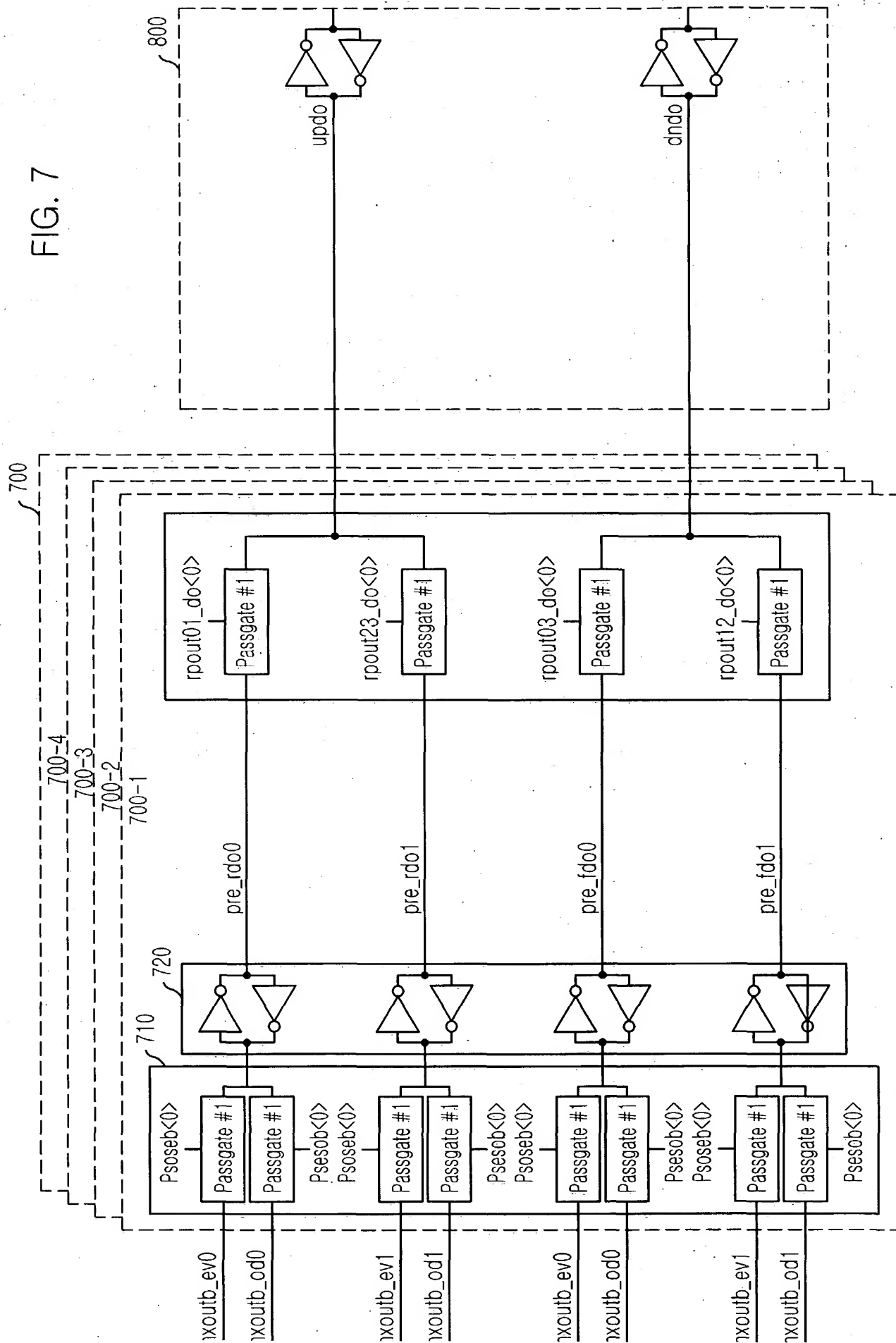


FIG. 8A

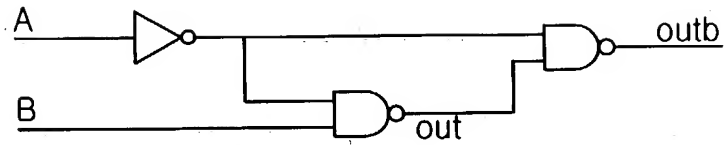


FIG. 8B

	A:"H" B:don't care	A:"L" B:"H"	A:"L" B:"L"
out	High	Low	High
outb	High	High	Low